

PATENT
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Appl. No. : 10/749,096
Applicant : Rafael Reif et al.
Filed : December 30, 2003
T.C./A.U. : 3745
Examiner : Not Yet Assigned

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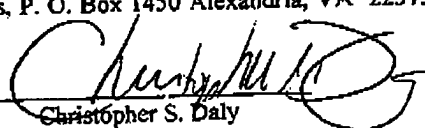
Docket No. : MIT-136DCUS
Customer No. : 022494

Certificate of Mailing (37 C.F.R. 1.8(a))

I hereby certify that this correspondence is being transmitted via facsimile transmission to the:
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15 JUL 04
Date of Signature
and Fax Transmission

By:


Christopher S. Daly
Reg. No. 37,303

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is desired to cite for the record in this application the enclosed documents listed on the attached copy of PTO form PTO/SB/08A (Formerly #1449). The paragraph(s) marked below are applicable to this Information Disclosure Statement.

[X] (1) The enclosed Information Disclosure Statement is being filed: within three months of the filing date; or within three months of the entry of the national stage of the above-identified application; or before the mailing of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. §1.114. Accordingly, Applicant(s) believes that no fee or statement is required.

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[] (2) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by the fee set forth in 37 C.F.R. § 1.17(p).

SUBMISSION UNDER 37 C.F.R. § 1.97(c)

INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(1)

[] (3) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(c)

INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(2)

[] (4) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56 (c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

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SUBMISSION UNDER 37 C.F.R. § 1.97(c)INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[] (5) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(1)

[] (6) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(2)

[] (7) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to

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any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

SUBMISSION UNDER 37 C.F.R. § 1.97(d)

INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[] (8) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

The filing of this Information Disclosure Statement is not a representation by the undersigned as to personal knowledge of the contents of every word or phrase of the material enclosed or that reliance on other suitably trained professionals has not been made.

If a search report of a searching agency is enclosed identifying the nature of the relevance of each document, such a designation is deemed to satisfy Rule 98(a) (3) even if in a foreign language, since the few terms of relevance therein are deemed of universal cognizance. However, Applicant(s) does not necessarily adopt the position reflected by that report.

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The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 500845.

Respectfully submitted,

Dated: 13 JUL 04

DALY, CROWLEY & MOFFORD, LLP

By: 

Christopher S. Daly

Reg. No. 37,303

Attorney for Applicant(s)

275 Turnpike Street, Suite 101

Canton, MA 02021-2354

Tel.: (781) 401-9988, ext. 11

Fax: (781) 401-9966

csd@dc-m.com

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Approved for use through 04/30/2003. OMB 0551-0031

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/749,096
Filing Date	December 30, 2003
First Named Inventor	Rafael Reif
Art Unit	3745
Examiner Name	Not Yet Assigned
Attorney Docket Number	MIT-136DUS

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		OSAMU TABATA, Anisotropic Etching of Silicon in TMAH Solutions, Toyota Central Research and Development Laboratories, Inc., February 21, 1992, Pages 51-57, Japan	
		PHILIP M. SAILER, Creating 3D Circuits Using Transferred Films, Circuits & Devices, November 1997, Pages 27-30	
		TAKUJI MATSUMOTO, New Three-Dimensional Wafer Bonding Technology Using the Adhesive Injection Method, March, 1998, Pages 1217-1221, Jpn. J. Appl. Phys. Vol. 37 (1998)	
		A. FAN, Copper Wafer Bonding, Electrochemical and Solid-State Letters, 1998, Pages 534-536, Cambridge, Massachusetts	
		ARIFUR RAHMAN, Wire-Length Distribution of Three-Dimensional Integrated Circuits, 1999, Pages 233-235	
		VICTOR W.C. CHAN, Three Dimensional CMOS Integrated Circuits on Large Grain Polysilicon Films, IEEE, Hong Kong University of Science and Technology, 2000, Pages 00-161 - 00-164 Hong Kong	
		K.W. LEE, Three-Dimensional Shared Memory Fabricated Using Wafer Stacking Technology, Dept. of Machine Intelligence and Systems Engineering, 2000, Pages 00-165 - 00-168, Hong Kong	
		KUAN-NENG CHEN, Microstructure Examination of Copper Wafer Bonding, Microsystem Technology Laboratories, December 20, 2000, Pages 331-335, Cambridge, Massachusetts	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: MS Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

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